

In re the Application of:

TI-36220

Anindya Saha, et al.

Art Unit: 2816

Serial No:

10/710,861

Examiner:

Filed:

August 9, 2004

Conf. No.: 4860

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For:

Providing Optimal Supply Voltage to Integrated Circuits

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Robin E. Barnum

on April 11, 2005.

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith. The relevancy of each of the references is noted on the enclosed Search Reports.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,

Abdul Zindani

Attorney for Applicant

Reg. No. 46,091

Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 (972) 917-5137 Approved fo

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	INFORMATIO	ON DISCLOSURE	<b>\$</b> /	Filing Date	August 9, 2004		
		BY APPLICANO 1	7 5002 m	First Named Inventor	Anindya Saha, et al.		
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Sheet	1	of Of	DEMAR	Attorney Docket No.	TI-36220		
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				U.S. PATE	NT DOCUMENTS	
		U.S. Patent Document		Name of Patentee or Applicant	Date of Pub. of	
Exam. Initials*	Cite No.1	Number	Kind Code <sup>2</sup> (if known)	of Cited Doc.	Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	6.211.727 B		Carobolante	4/3/01	
	AB	5.648.766	•	Stengel, et al.	7/15/97	
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Exam. Initials*		Foreign Patent Document			Name of Patentee or	Date of Pub. of		
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	BA	JP	10322019a		NEC Nigata Ltd	5/21/97	Abstract	
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
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	CA				
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